Attorney's Docket No.: 119128-161706

145404

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

John et al.

Application No.: 09/741,616

Filed: 19-Dec-2000

Patent No.: 7,107,434

Issued: 12-Sep-2006

For: SYSTEM, METHOD AND

APPARATUS FOR ALLOCATING HARDWARE RESOURCES USING PSEUDORANDOM

**SEQUENCES** 

Examiner: Pan, Daniel H.

Patent

Art Group: 2183

Confirmation No. 5809

Attn: Certificate of Corrections Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

Upon review of the above-referenced Letters Patent, Applicant noted the errors are as follows:

Column 8

Line 65, "Bit b0=0, Bit b1=0, ...and Bit b15=0" should read --..Bit b0=1, Bit b1=1, Bit b2= 1, Bit b3=1, Bit b4=0, Bit b5=1, Bit b6=0, Bit b7=1, Bit b8=1, Bit b9=0, Bit b13=0, Bit b14=0, Bit b15=0..--.

Column 9

Line 29, "...by connecting by connecting..." should read --...by connecting...-.

Column 10

Line 52, "... Table II..." should read --... Table III...-.

Enclosed is a Certificate of Correction (in duplicate) and it is respectfully requested that a Certificate of Correction be issued. This request for correction is made under the provisions of 37 CFR 1.322 and 1.323.

Payment by Deposit Account No. 500393 is included in the amount of \$100.00 for the fee as set forth in 37 CFR § 1.20(a).

Respectfully submitted, SCHWABE, WILLIAMSON & WYATT, P.C.

Dated: August 12, 2008 /Linda S. Zachariah/

Linda S. Zachariah Reg. No. 48,057

Pacwest Center, Suite 1900 1211 SW Fifth Avenue Portland, Oregon 97204 Telephone: 503-222-9981

Patent No.: 7,107,434

# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page \_1\_ of \_1

PATENT NO. : 7,107,434

APPLICATION NO.: 09/741,616
ISSUE DATE : 12-Sep-2006
INVENTOR(S) : John et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

#### Column 8

Line 65, "Bit b0=0, Bit b1=0, ...and Bit b15=0" should read --..Bit b0=1, Bit b1=1, Bit b2= 1, Bit b3=1, Bit b4=0, Bit b5=1, Bit b6=0, Bit b7=1, Bit b8=1, Bit b9=0, Bit b13=0, Bit b14=0, Bit b15=0...-.

#### Column 9

Line 29, "...by connecting by connecting..." should read --...by connecting...-.

#### Column 10

Line 52, "...Table II..." should read --...Table III...--.

MAILING ADDRESS OF SENDER (Please do not use customer number below): SCHWABE, WILLIAMSON & WYATT, P.C., Pacwest Center, Suite 1900, 1211 SW Fifth Avenue, Portland, Oregon 97204

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**